

SyncSim Platform independent Extensible and general

GUI

Easy to use Responsive Source code editor

Simulator

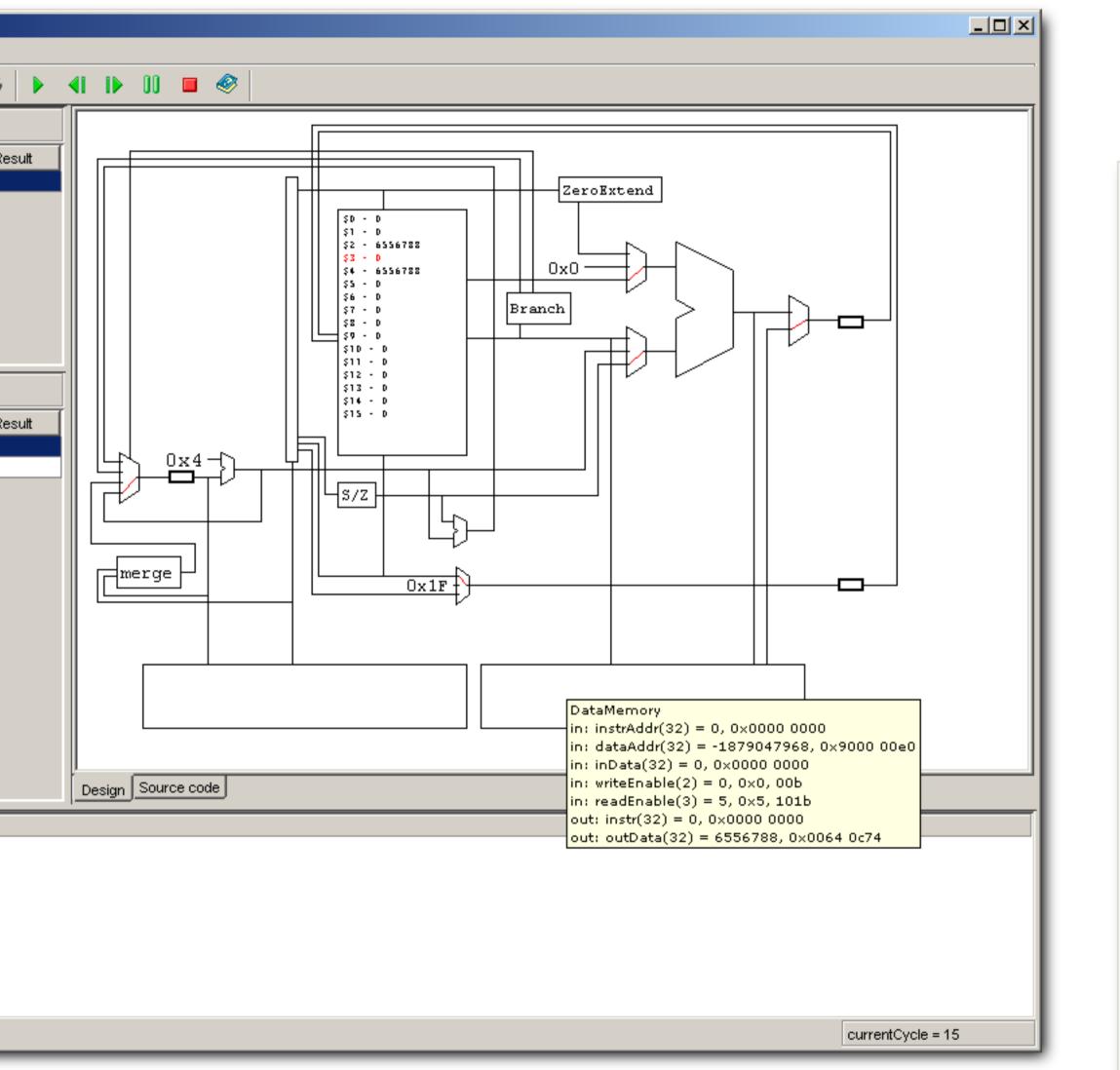
Breakpoint expressions Watches Forward and backward step functionality

MIPS model

Good graphical layout Run a subset of MIPS code Forseeable

A SMD153 project by: Erik Byström, Lars Magnusson and Robert Selberg

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Breakpoints			
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SyncSim GUI displaying a MIPS implementation.

int my int mu retu ffffff: ffffff ffffff: ffffff ffffff ffffff ffffff ffffff ffffff ffffff int ma intffffff ffffff ffffff _ Code

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(美)		비즈
ffffffff80020090 g 0 *ABS*000000000000000	bss_start	
ffffffff80020040 g F .text000000000000048	main	
ffffffff80020090 g 0 .sbss000000000000000	sbss_end	
ffffffff80020090 g 0 *ABS*000000000000000	_edata	
ffffffff80020090 g 0 *ABS*0000000000000000	_end	
ffffffff80020088 g 0 .text0000000000000000	_ecode	
ffffffff80020090 g 0 *ABS*0000000000000000	_fbss	
ffffffff8002008c g 0 .sdata000000000000004	nystik	
Disassembly of section .text:		
-		
ffffffff80020000 <mul_fi>:</mul_fi>		
ffffffff80020000:27bdfff8 addiu\$sp,\$sp,-8		
ffffffff80020004:afbe0000 sw\$s8,0(\$sp)		
ffffffff80020008:03a0f025 move\$s8,\$sp		
ffffffff8002000c:afc40008 sw\$a0,8(\$s8)		
int test = 65;		
int mystik = 2;		
int mul(int in) (
return in+test+mystik;		
ffffffff80020010:8fc30008 lw\$v1,8(\$s8)		
ffffffff80020014:8f828008 lw\$v0,-32760(\$gp)		
ffffffff80020018:00000000 nop		
fffffffff8002001c:00621021 addu\$v0,\$v1,\$v0		
ffffffff80020020:8f83800c lw\$v1,-32756(\$gp)		
ffffffff80020024:00000000 nop		
ffffffff80020028:00431021 addu\$v0,\$v0,\$v1		
}		
, ffffffff8002002c:03c0e825 move\$sp,\$s8		
ffffffff80020030:8fbe0000 lw\$s8,0(\$sp)	🛓 📃 🔟 🖄	
	Address: 80020010	
ffffffff80020034:27bd0008 addiu\$sp,\$sp,8	0x80020010: 8fc30008	
ffffffff80020038:03e00008 jr\$ra	0x80020014: 8f828008	
ffffffff8002003c:00000000 nop	0x80020018: 00000000	
ffffffff80020040 <main>:</main>	0x8002001c: 00621021	
ffffffff80020040:27bdffe0 addiu\$sp,\$sp,-32	0x80020020: 8f83800c 0x80020024: 00000000	
ffffffff80020044:afbf00lc sw\$ra,28(\$sp)	0x80020028: 00431021	
ffffffff80020048:afbe0018 sw\$s8,24(\$sp)	0x8002002c: 03c0e825	
ffffffff8002004c:03a0f025 move\$s8,\$sp	0x80020030: 8fbe0000	
ffffffff80020050:afc40020 sw\$a0,32(\$s8)	0x80020034: 27bd0008	
ffffffff80020054:afc50024 sw\$al,36(\$s8)	0x80020038: 03e00008	
int main(int argc, char **argv) {	0x8002003c: 00000000	
int a = 6556788;	0x80020040: 27bdffe0 0x80020044: afbf001c	
ffffffff80020058:3c020064 lui\$v0,0x64	0x80020044: afbf0010 0x80020048: afbe0018	
ffffffff8002005c:34420c74 ori\$v0,\$v0,0xc74	0x8002004c: 03a0f025	
	0x80020050: afc40020	
ffffffff80020060:afc20010 sw\$v0.16(\$s8)	0x80020054: afc50024	
Code view with intermixed	0x80020058: 3c020064	
	0x8002005c: 34420c74	
assembly and c code	0x80020060: afc20010 0x80020064: 8fc40010	
	0x80020068: 0c008000	
	0x8002006c: 00000000	
	0x80020070: 03c0e825	
	0x80020074: 8fbf001c	
	0x80020078: 8fbe0018	
	0x8002007c: 27bd0020	
	0x80020080: 03e00008	
	0x80020084: 00000000 0x80020088: 00000041	
	0x8002008c: 00000002	

Data view

0x80020090: 00000000

